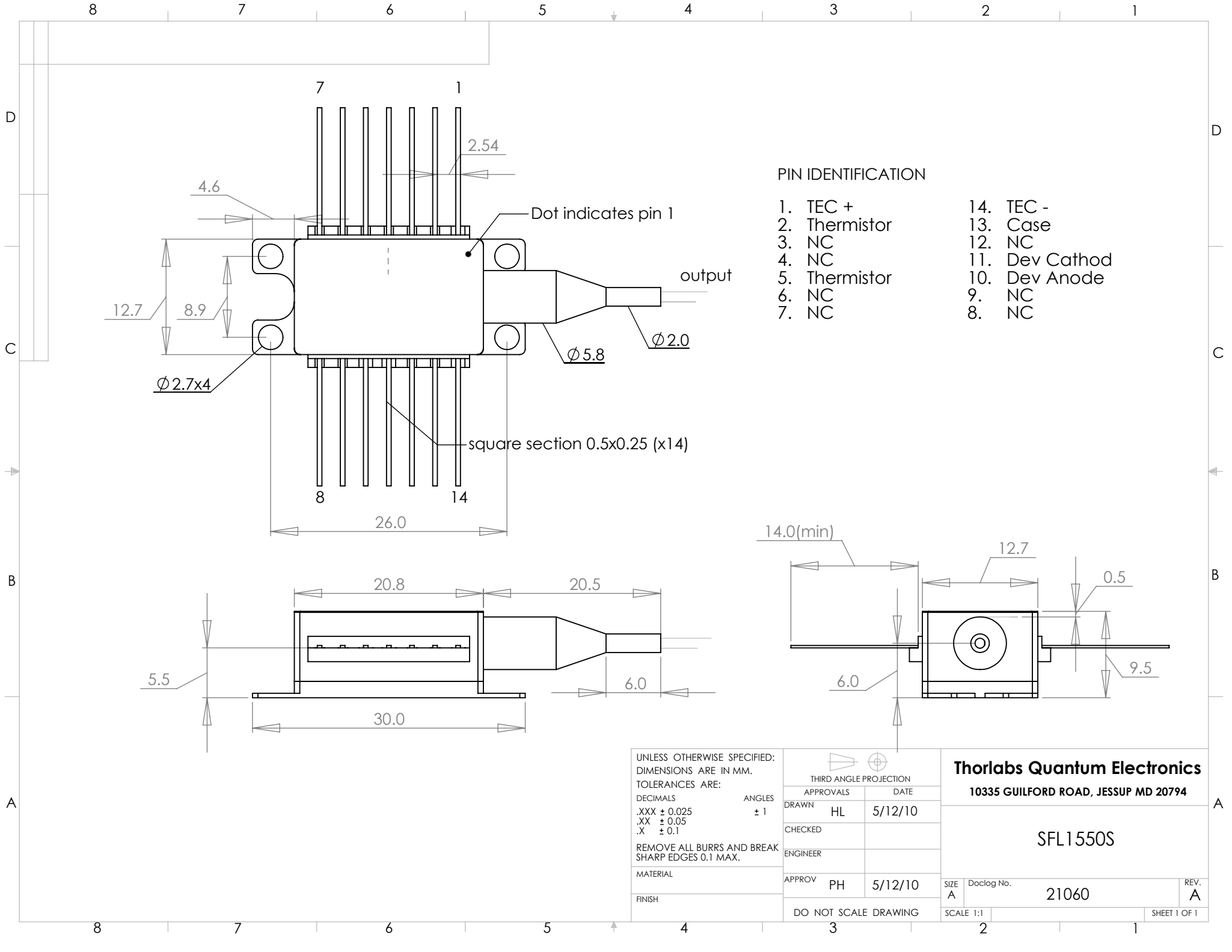




PIN IDENTIFICATION

- | | |
|---------------|----------------|
| 1. TEC + | 14. TEC - |
| 2. Thermistor | 13. Case |
| 3. NC | 12. NC |
| 4. NC | 11. Dev Cathod |
| 5. Thermistor | 10. Dev Anode |
| 6. NC | 9. NC |
| 7. NC | 8. NC |

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MM. TOLERANCES ARE: DECIMALS .XXX ± 0.025 .XX ± 0.05 .X ± 0.1 ANGLES ± 1 REMOVE ALL BURRS AND BREAK SHARP EDGES 0.1 MAX. MATERIAL FINISH		THIRD ANGLE PROJECTION APPROVALS DATE DRAWN HL 5/12/10 CHECKED ENGINEER APPROV PH 5/12/10 DO NOT SCALE DRAWING		Thorlabs Quantum Electronics 10335 GUILFORD ROAD, JESSUP MD 20794 SFL1550S SIZE A Doclog No. 21060 REV. A	
				SCALE 1:1	SHEET 1 OF 1